



## Academic Personnel Short Profile / Short CV

|                           |   |
|---------------------------|---|
| <b>University:</b>        | Frederick University                                |
| <b>Surname:</b>           | Tatas   |
| <b>Name:</b>              | Konstantinos  |
| <b>Rank/Position:</b>     | Associate Professor                                 |
| <b>School:</b>            | Engineering and Applied Sciences                    |
| <b>Department:</b>        | Electrical and Computer Engineering and Informatics |
| <b>Scientific Domain:</b> | Computer Architecture and Embedded Systems          |

### Academic qualifications

| Qualification | Year | Awarding Institution                    | Department                          | Thesis title (Optional Entry)                            |
|---------------|------|---|-------------------------------------|--|
| PhD           | 2005 | Democritus University of Thrace, Greece | Electrical and Computer Engineering | VLSI Design of DSP Systems with Low Power Consumption    |
| BSc           | 1999 | Democritus University of Thrace, Greece | Electrical and Computer Engineering | Design and Implementation of Arithmetic Units using VHDL |

### Employment history in Academic Institutions/Research Centers

| Period of employment |      | Employer  | Location             | Position   |
|----------------------|------|---|----------------------|--|
| From                 | To   |   |                      |  |
| 2007                 | Now  | Frederick University                              | Nicosia              | Lecturer/Assistant Professor/Associate Professor |
| 2007                 | 2007 | Frederick Institute of Technology                 | Nicosia              | Lecturer   |
| 2006                 | 2007 | Alexander Technological Institute of Thessaloniki | Thessaloniki, Greece | Laboratory Instructor                            |

|      |      |                                |                       |   |
|------|------|--------------------------------|-----------------------|---|
| 2000 | 2003 | INTRACOM SA<br>Research Center | Xanthi/Athens, Greece | Digital Integrated Circuit Designer, Researcher |
|------|------|--------------------------------|-----------------------|---|

| <b>Key <i>refereed</i> journal papers, monographs, books, conference publications etc.</b> |             |   |  |  |             |              |
|--|-------------|---|--|--|-------------|--------------|
| <b>Ref. Number</b>   | <b>Year</b> | <b>Title</b>  | <b>Other authors</b>   | <b>Journal and Publisher / Conference</b>                              | <b>Vol.</b> | <b>Pages</b> |
| 1  | 2023        | Approximate Priority Hybrid 3DNoC Buffered-Bufferless Router  | Savvas Savva, Costas Kyriacou  | Micromachines, MDPI  | 14(2)       |              |
| 2  | 2022        | Performance Analysis of 2D and 3D Bufferless NoCs Using Markov Chain Models   | –  | Technologies, MDPI   | 10(1)       |              |
| 3  | 2022        | Reliable IoT-Based Monitoring and Control of Hydroponic Systems   | Ahmad Al-Zoubi, Nicholas Christofides, Chrysostomos Zannettis, Michael Chrysostomou, Stavros Panteli, and Anthony Antoniou | Technologies, MDPI   | 10(1)       |              |
| 4  | 2020        | Fuzzy classification of OpenCL programs targeting heterogeneous systems   | Ahmad Al-Zoubi, Costas Kyriacou  | Journal of Intelligent & Fuzzy Systems, IOS Press                      | 39(5)       | 7189-7202    |
| 5  | 2017        | Hardware implementation of dynamic fuzzy logic based routing in Network-on-Chip                                     | Chrysostomos Chrysostomou  | Microprocessors and Microsystems - Embedded Hardware Design (Elsevier) | 52          | 80-88        |
| 7  | 2014        | Designing 2D and 3D Network-on-Chip Architectures   | Kostas Siozios, Dimitrios Soudris, Axel Jantsch  | Springer   |             |              |
| 8  | 2007        | Architecture design of a coarse-grain reconfigurable multiply-accumulate unit for data-intensive applications       | George Koutroumpetis, Dimitrios Soudris, Antonios Thanailakis  | Integration, the VLSI Journal, Elsevier                                | 40(2)       | 74-93        |
| 9  | 2006        | Behavioral-Level Performance and Power Exploration of Data-Intensive Applications Mapped on Programmable Processors | Nikolaos Kroupis, Nikolaos D. Zervas, Minas Dasygenis, Antonios Thanailakis  | Journal of VLSI Signal Processing, Springer                            | 44          | 153-171      |
| 10   | 2005        | A complete platform and toolset for   | Vasilis Kalenteridis, H.   | Microprocessors and  | 29(6)       | 247-         |

|  |  |   |   |                        |  |     |
|--|--|---|---|------------------------|--|-----|
|  |  | system implementation on fine-grain reconfigurable hardware | Pournara, Kostas Siozios, Dimitrios Soudris, Antonios Thanailakis | Microsystems, Elsevier |  | 259 |
|--|--|---|---|------------------------|--|-----|

| <b>Research Projects</b> |             |   |   |                        |
|--------------------------|-------------|---|---|------------------------|
| <b>Ref. Number</b>       | <b>Date</b> | <b>Title</b>  | <b>Funded by</b>  | <b>Project Role</b>    |
| 1                        | 2019-2021   | iPONICS: SmartOff-Grid System for Sustainable Hydroponics   | Cyprus Research and Innovation Foundation                 | Scientific Coordinator |
| 2                        | 2010-2012   | SYStematic design of 3D Multicore Architectures with NeTwork-on-chip Communication (SYSMANTIC)                      | Cyprus Research Promotion Foundation                      | Principal Researcher   |
| 3                        | 2005-2008   | DDM-CMP: Data Driven Multithreading on a Chip Multiprocessor, Development and Implementation                        | Cyprus Research Promotion Foundation                      | Researcher             |
| 4                        | 2002-2005   | AMDREL: "Architectures and Methodologies for Dynamic REconfigurable Logic   | EC 5th IST Framework                                      | Researcher             |
| 5                        | 2001-2004   | EASY: Energy-Aware SYstem-on-chip design of the HIPERLAN/2 standard   | EC 5th IST Framework                                      | Researcher             |
| 6                        | 2004-2005   | Wireless Subscriber's Connections of Advanced Services Access Network Implemented in Silicon (MILI-A, Wireless mile | General Secretariat for Research and Technology - Greece. | Researcher             |
| 7                        | 2001-2003   | Memory Management Methodology for Real-Time and Low-Power   | General Secretariat for                                   | Researcher             |

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|--|--|---|----------------------------------|--|
|  |  | Consumption Embedded Multimedia Systems | Research and Technology - Greece |  |
|--|--|---|----------------------------------|--|

| <b>Academic Consulting Services and/or Participation in Councils / Boards/ Editorial Committees</b> |               |  |                                     |   |
|---|---------------|--|-------------------------------------|---|
| <b>Ref. Number</b>  | <b>Period</b> | <b>Organization</b>                                    | <b>Title of Position or Service</b> | <b>Key Activities</b>   |
| 1   | 2018-2020     | Cyprus Ministry of Transport, Communications and Works | Advisor                             | Co-author of Cyprus national strategy for Artificial Intelligence |